Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1 .	74	((((((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (compar\$4 XOR EXOR exclusive\$2 X-OR EX-OR match dismatch mismatch dis-match mis-match)) and (complement\$3 invert\$3 prime "NOT" invers\$ revers\$3)) and ((data near5 width) (bus near5 width))) and respons\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:57
S54	70	((((((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (compar\$4 XOR EXOR exclusive\$2 X-OR EX-OR match dismatch mismatch dis-match mis-match)) and (complement\$3 invert\$3 prime "NOT" invers\$ revers\$3)) and ((data near5 width) (bus near5 width))) and respons\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:56
S10 8	2	"6563739".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/18 14:01
S10 4	2	"6279114".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/18 14:01
S10 7	1	"5774546".PN.	US-PGPUB; USPAT	OR	ON	2004/08/18 13:28
S10 6	1	"5930496".PN.	US-PGPUB; USPAT	OR	ON	2004/08/18 13:28
S10 5	1	"5958056".PN.	US-PGPUB; USPAT	OR	ON	2004/08/18 13:27
S10 3	2	"6040622".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/18 13:26
S87	17	((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (((memory (electronic near2 circuit) multimedia mult-imedia "multi media") near4 card) MMC) and ((bus data bit) near6 width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/18 13:21

C10		F360040	Luc Bosis	00		
S10 2	2	"5260949".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/18 09:50
S10 1	0	(714/823,735).ccls. and complement and repons\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/17 14:03
S10 0	0	((714/823,735).ccls.) and complement and repons\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/17 14:03
S99	52	((714/823,735).ccls.) and complement andrepons\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/17 14:03
S98	0	((714/823,735).ccls.) and (complement same repons\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/17 14:02
S97	52	((714/823,735).ccls.) and complement	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/17 14:02
S96	402	((714/823,735).ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/17 13:51
S89	2118	((714/712,823,750).ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/17 13:49
S95	76	(((714/712,823,750).ccls.)) and ((((data bit pattern) near5 compar\$4) (bit near5 pattern near5 compar\$4)) same memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/17 10:13
S93	370	(((714/712,823,750).ccls.)) and (((data bit pattern) near5 compar\$4) (bit near5 pattern near5 compar\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/17 10:13

S94	177	((((714/712,823,750).ccls.)) and (((data bit pattern) near5 compar\$4) (bit near5 pattern near5 compar\$4))) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/17 10:12
S92	34	(((714/712,823,750).ccls.)) and ((((data bit pattern) near5 compar\$4) (bit near5 pattern near5 compar\$4)) same (program software algorithm))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR ·	ON	2004/08/17 10:08
S91	224	((((714/712,823,750).ccls.)) and (((data bit pattern near5 compar\$4) (bit near5 pattern near5 compar\$4)) same (program software algorithm))) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/17 09:42
S90	350	(((714/712,823,750).ccls.)) and (((data bit pattern near5 compar\$4) (bit near5 pattern near5 compar\$4)) same (program software algorithm))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/17 09:42
S88	1	((714/712,823,750).ccls.) and (((memory (electronic near2 circuit) multimedia mult-imedia "multi media") near4 card) MMC) and ((bus data bit) near6 width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/17 09:39
S86	239	((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (((memory (electronic near2 circuit) multimedia mult-imedia "multi media") near4 card) MMC) and (bus data bit near6 width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 17:32
S42	242	((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (((memory (electronic near2 circuit) multimedia mult-imedia "multi media") near4 card) MMC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 17:27
S85	10	(( (((send\$3 transmit\$4) near4 (pattern data bit)) same (receiv\$3 near6 (respond response)))) and (host same card)) and compliment	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 16:52
S84	611	( (((send\$3 transmit\$4) near4 (pattern data bit)) same (receiv\$3 near6 (respond response)))) and (host same card)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 16:51

COO	10000	(((====4+2.1======4+4)	110 555::-	T ==		2004/20/15
S83	16965	(((send\$3 transmit\$4) near4 (pattern data bit)) same (receiv\$3 near6 (respond response)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 16:51
S82	15	((host same (memory near4 card)). ti,ab.) and (((send\$3 transmit\$4) near4 (pattern data bit)) same (receiv\$3 near6 (respond response)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON .	2004/08/16 16:46
S60	910	(host same (memory near4 card)). ti,ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 16:24
S81	1	"5528758".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:17
S80	1	"5559965".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:16
S79	1	"5559965".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:16
S78	1	"5572683".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:16
S77	1	"5586270".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:16
S76	1	"5600801".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:15
S75	1	"5613092".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:15
S74	1	"5802325".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:15
S73	1	"5928347".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:14
S72	1	"5598540".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:12
S71	1	"5371736".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:12
S70	1	"5742502".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:12
S69	1	"5500517".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:12
S68	1	"5594233".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:12
S67	1	"5594233".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:11
S66	1	"6012634".PN.	US-PGPUB; USPAT	OR	ON	2004/08/16 16:05

S65	38	((host near4 (memory near4 card)). ti,ab.) and ((number amount) near4 data)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 15:55
S64	16	((host near4 (memory near4 card)). ti,ab.) and width	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 15:54
S63	311	(host near4 (memory near4 card)). ti,ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 15:37
S62	4	(host near4 (memory near4 card)). ti,ab. and ((bus near4 width) (parallel near2 lines))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 15:04
S61	0	((host same (memory near4 card)). ti,ab.) and (((determin\$3 check\$3 test\$3 detect\$3) near5 ((data near5 width) (bus near5 width))).ti,ab.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 15:02
S59	910	(host same memory near4 card).ti, ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 15:00
S58	3339	host same memory near4 card	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 15:00
S1	22941	(host near4 (device \$4process\$3)) and (memory near4 (card device))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 14:59
S57	5	(((determin\$3 check\$3 test\$3 detect\$3) near5 ((data near5 width) (bus near5 width))).ti,ab.) and (loopback loop-back loop near2 back)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 14:07
S56	1481	((determin\$3 check\$3 test\$3 detect\$3) near5 ((data near5 width) (bus near5 width))).ti,ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 14:06

S55	4829	((determin\$3 check\$3 test\$3 detect\$3) near5 ((data near5	US-PGPUB; USPAT;	OR	ON	2004/08/16 13:43
		width) (bus near5 width)))	EPO; JPO; DERWENT; IBM_TDB			
S53	85	((((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (compar\$4 XOR EXOR exclusive\$2 X-OR EX-OR match dismatch mismatch dis-match mis-match)) and (complement\$3 invert\$3 prime "NOT" invers\$ revers\$3)) and ((data near5 width) (bus near5 width))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 13:41
S52	1819	(((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (compar\$4 XOR EXOR exclusive\$2 X-OR EX-OR match dismatch mismatch dis-match mis-match)) and (complement\$3 invert\$3 prime "NOT" invers\$ revers\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 15:10
S48	15	(((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (((memory (electronic near2 circuit) multimedia mult-imedia "multi media") near4 card) MMC)) and ((data near5 width) (bus near5 width))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 15:10
S51	4755	((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (compar\$4 XOR EXOR exclusive\$2 X-OR EX-OR match dismatch mismatch dis-match mis-match)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 15:09
S41	68	((((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and ((memory (electronic near2 circuit)) near4 card)) and (compar\$4 XOR EXOR exclusive\$2 X-OR EX-OR match dismatch mismatch dis-match mis-match)) and (complement\$3 invert\$3 prime "NOT" invers\$ revers\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 15:09

S40	171	(((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and ((memory (electronic near2 circuit)) near4 card)) and (compar\$4 XOR EXOR exclusive\$2 X-OR EX-OR match dismatch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 15:09
S50	5	mismatch dis-match mis-match) ((((710/104 714/712,715,819,820,821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (((memory (electronic near2 circuit) multimedia mult-imedia "multi media") near4 card) MMC)) and ((data near5 width) (bus near5 width))) not ((((710/104 714/712,715,819,820,821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (((memory (electronic near2 circuit) multimedia mult-imedia "multi media") near4 card) MMC)) and ((data near5 width) (bus near5 width))) and	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 14:17
S49	15	response)  ((((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (((memory (electronic near2 circuit) multimedia mult-imedia "multi media") near4 card) MMC)) and ((data near5 width) (bus near5 width))) not n22	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 14:17
S47	1	"5065154".PN.	US-PGPUB; USPAT	OR	ON	2004/08/13 14:13
S46	10	((((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (((memory (electronic near2 circuit) multimedia mult-imedia "multi media") near4 card) MMC)) and ((data near5 width) (bus near5 width))) and response	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 13:55
S45	11686	(((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (((memory (electronic near2 circuit) multimedia mult-imedia "multi media") near4 card) MMC)) and (data near5 width) (bus near5 width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 13:54

S44	70	((((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (((memory (electronic near2 circuit) multimedia mult-imedia "multi media") near4 card) MMC)) and (compar\$4 XOR EXOR exclusive\$2 X-OR EX-OR match dismatch mismatch dis-match mis-match)) and (complement\$3 invert\$3 prime "NOT" invers\$ revers\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 12:22
S43	177	(((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (((memory (electronic near2 circuit) multimedia mult-imedia "multi media") near4 card) MMC)) and (compar\$4 XOR EXOR exclusive\$2 X-OR EX-OR match dismatch mismatch dis-match mis-match)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 11:59
S39	233	((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and ((memory (electronic near2 circuit)) near4 card)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 11:57
S38	228	((710/104 714/712,715,819,820, 821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.) and (memory near4 card)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 11:46
S37	8818	(710/104 714/712,715,819,820,821, 823,750,748,746 709/237 455/67. 11,67.13,67.14,226.1).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 11:30
S36	0	"09641023".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 11:29
S28	8121	(714/712,715,819,820,821,823,750,748,746 709/237 455/67.11,67.13,67.14,226.1).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 11:29
S35	2	"9641023".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 11:10

S34	0	"641023.ap"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 11:10
S33	0	"09641023.ap"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 11:10
S32	0	"9641023.ap"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 11:10
S31	0	\$0215020\$did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 11:09
S30	0	WO\$0215020\$did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 11:04
S29	0	WO-0215020\$did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 11:03
S27	4	("4559329" "4616039").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 10:48
S26	0	("4559329" and "4616039").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR ,	ON	2004/08/13 10:15
S25	18	(( ((process\$3 microprocess\$3 micro-process\$3 microcomputer micro-computer) same (memory near5 card))) and ((test\$3 check\$3) near6 (data near3 bus))) and (((test bit) near4 (pattern data)) same (complement reverse))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 10:15

S24	7	(( ((process\$3 microprocess\$3 micro-process\$3 microcomputer micro-computer) same (memory near5 card))) and ((test\$3 check\$3) near6 (data near3 bus))) and (((test bit) near4 (pattern data)) same complement)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 15:33
S23	138	( ((process\$3 microprocess\$3 micro-process\$3 microcomputer micro-computer) same (memory near5 card))) and ((test\$3 check\$3) near6 (data near3 bus))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 15:27
S12	118	(((host near4 (device \$4process\$3)) same (memory near4 (card device))) and complement) and (data near4 bus) and (((test bit) near4 (pattern data)) same complement)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 15:27
S22	4320	((test\$3 check\$3) near6 (data near3 bus))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 15:26
S21	25137	((process\$3 microprocess\$3 micro-process\$3 microcomputer micro-computer) same (memory near5 card))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 15:25
S16	921048	(device process\$3 microprocess\$3 micro-process\$3 microcomputer micro-computer) same memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 15:23
S20	41	(((( (device process\$3 micro-process\$3 microcomputer micro-computer) same memory) same test\$3 near4 pattern) and complement) and (handshak\$3 hand-shak\$3)) and data near4 bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 15:16
S17	3856	( (device process\$3 microprocess\$3 micro-process\$3 microcomputer micro-computer) same memory) same test\$3 near4 pattern	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 15:15
S19	50	((( (device process\$3 micro-process\$3 microcomputer micro-computer) same memory) same test\$3 near4 pattern) and complement) and (handshak\$3 hand-shak\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 15:14

Cic	405	///	110 205: :=	00	0	2004/00/42
S18	435	(( (device process\$3 micro-process\$3 microcomputer micro-computer) same memory) same test\$3 near4 pattern) and complement	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 15:14
S10	582	((host near4 (device \$4process\$3)) same (memory near4 (card device))) and complement	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 15:14
S9	715	((host near4 (device \$4process\$3)) same (memory near4 (card device))) and (handshak\$3 hand-shak\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 15:14
S3	7688	(host near4 (device \$4process\$3)) same (memory near4 (card device))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 15:10
S15	16	((((host near4 (device \$4process\$3)) same (memory near4 (card device))) and (handshak\$3 hand-shak\$3)) and phone) and complement	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 14:58
S14	103	(((host near4 (device \$4process\$3)) same (memory near4 (card device))) and (handshak\$3 hand-shak\$3)) and phone	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 14:58
S13	83	((((host near4 (device \$4process\$3)) same (memory near4 (card device))) and complement) and (data near4 bus) and (((test bit) near4 (pattern data)) same complement)) and ((boot start) near4 up)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 14:54
S11	407	(((host near4 (device \$4process\$3)) same (memory near4 (card device))) and complement) and (data near4 bus) and ((test bit) near4 (pattern data))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 14:52
S6	0	(((host near4 (device \$4process\$3)) same (memory near4 (card device))) same (test near5 (pattern data signal))) and complement and boot	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 14:49
S8	15	(((host near4 (device \$4process\$3)) same (memory near4 (card device))) same (test near5 (pattern data signal))) and (complement opposite invert\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 14:48

S7	15	(((host near4 (device \$4process\$3)) same (memory near4 (card device))) same (test near5 (pattern data signal))) and (omplement opposite invert\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 14:45
S5	63	((host near4 (device \$4process\$3)) same (memory near4 (card device))) same (test near5 (pattern data signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 14:44
S4	886	((host near4 (device \$4process\$3)) same (memory near4 (card device))) and (test near5 (pattern data signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 14:44
S2	2713	((host near4 (device \$4process\$3)) and (memory near4 (card device))) and (test near5 (pattern data signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 14:44